

11:SUBSTRATE 12:BASE FILM 13:SOURCE REGION 14:DRAIN REGION 15a -15d:LDD REGION  
 16:HIGH CONCENTRATION IMPURITY REGION 17a,17b:CHANNEL FORMING REGION 18:GATE INSULATING FILM  
 19a,19b:GATE ELECTRODE 20:FIRST INTERLAYER INSULATING FILM 21:SOURCE WIRING 22:DRAIN WIRING  
 23:GATE ELECTRODE 31:SOURCE REGION 32:DRAIN REGION 34:CHANNEL FORMING REGION 35:GATE ELECTRODE  
 36:SOURCE WIRING 37:DRAIN WIRING 38:FIRST PASSIVATION FILM 39:SECOND INTERLAYER INSULATING FILM  
 40:PIXEL ELECTRODE(ANODE) 41:BANK 42:EL LAYER 43:CATHODE 44:PROTECTING ELECTRODE  
 45:SECOND PASSIVATION FILM

FIG. 2

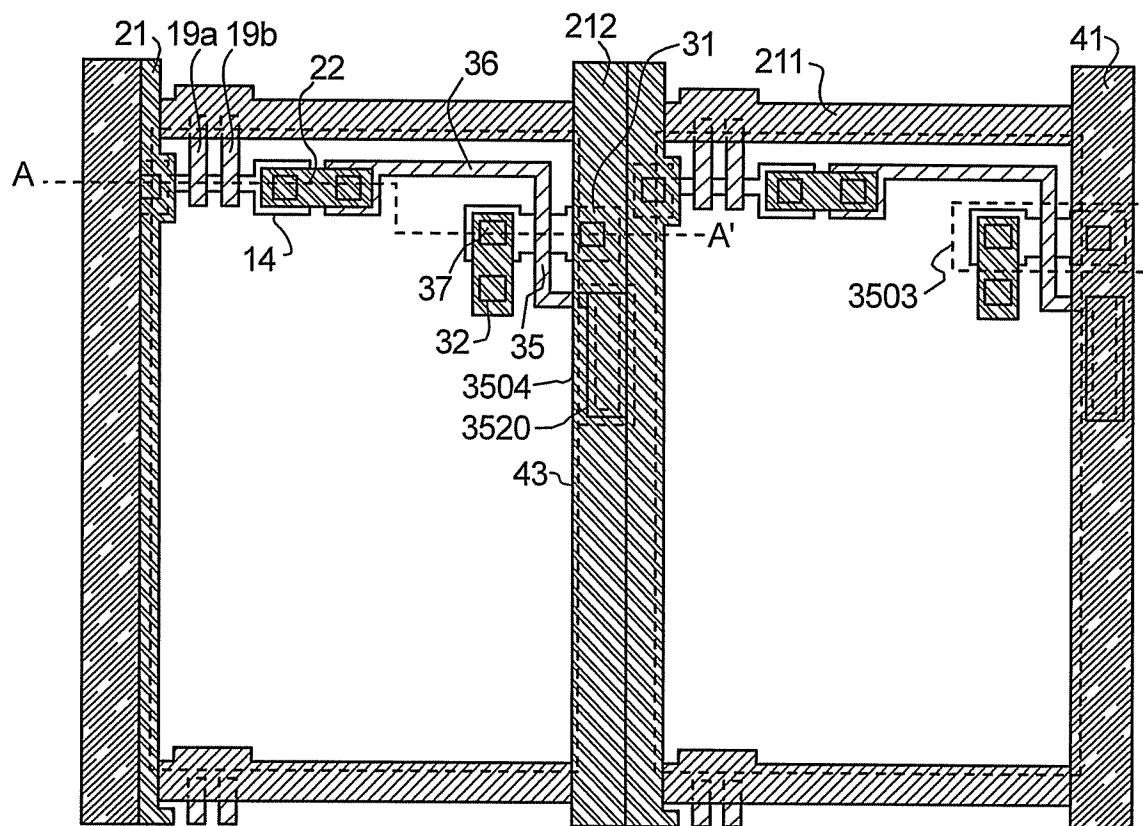


FIG. 3A

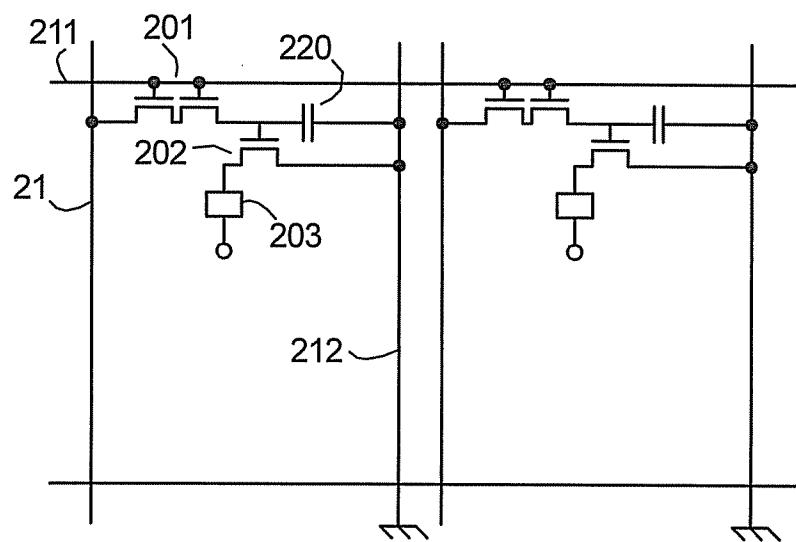


FIG. 3B

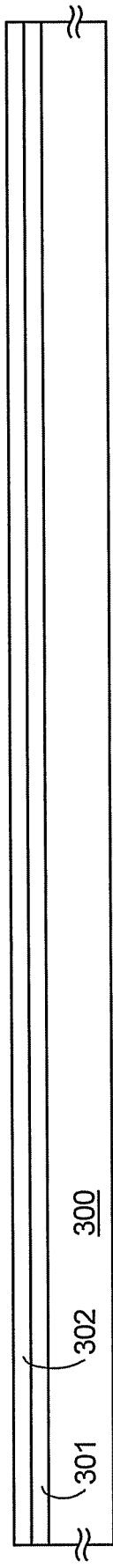


FIG. 4A

304b

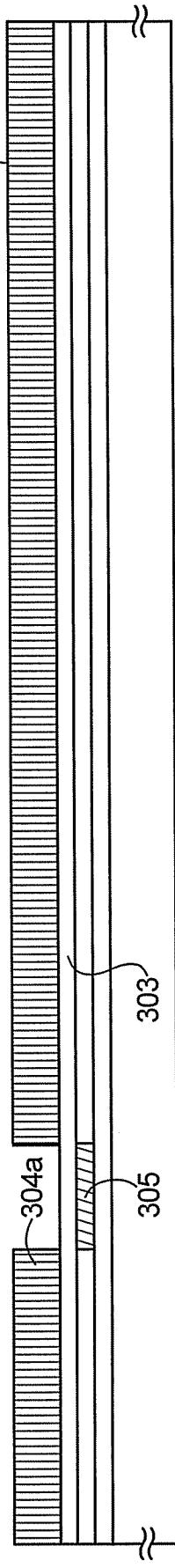


FIG. 4B

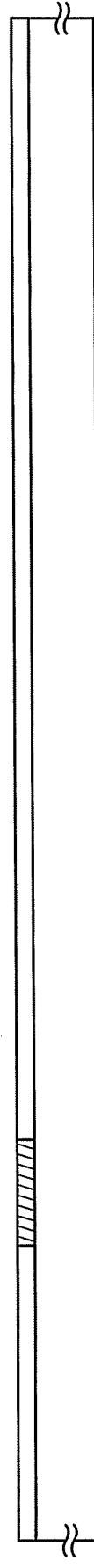


FIG. 4C

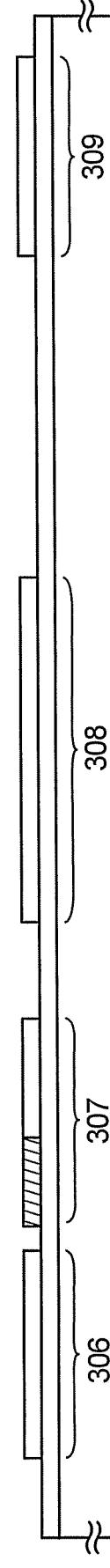


FIG. 4D

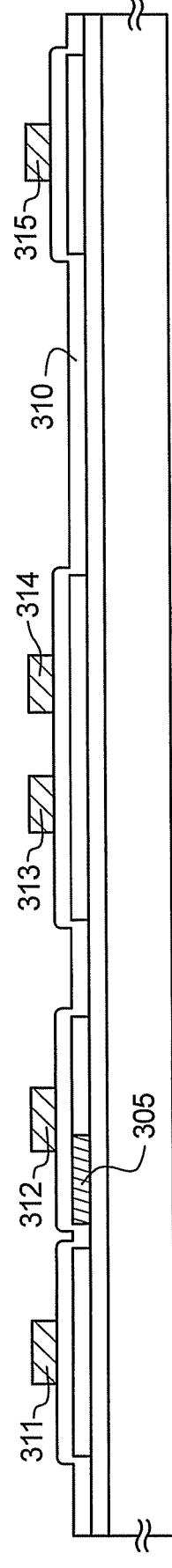


FIG. 4E

300:GLASS SUBSTRATE 301:BASE FILM 302:POLYSILICON FILM 303:PROTECTING FILM 304a,304b:RESIST MASK  
305:n-TYPE IMPURITY REGION 306-309:ACTIVE LAYER 310:GATE INSULATING FILM 311-315:GATE ELECTRODE

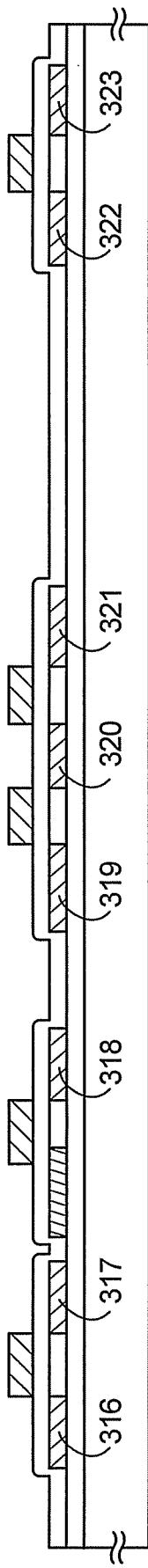


FIG. II-E. 5A

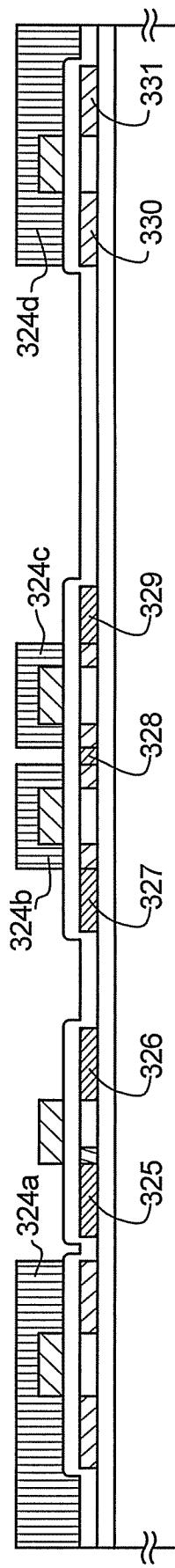


FIG. II-E. 5B

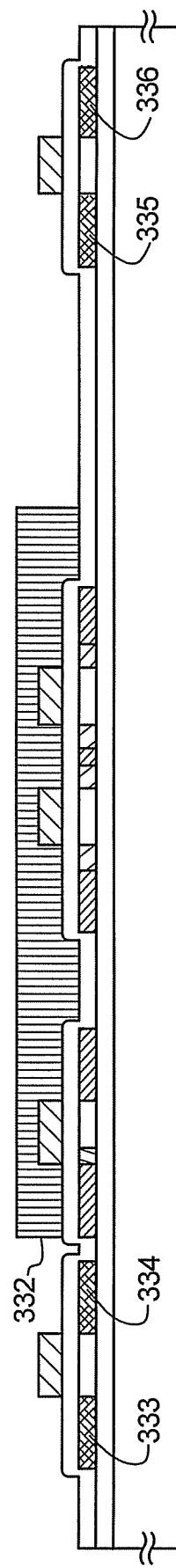


FIG. II-E. 5C

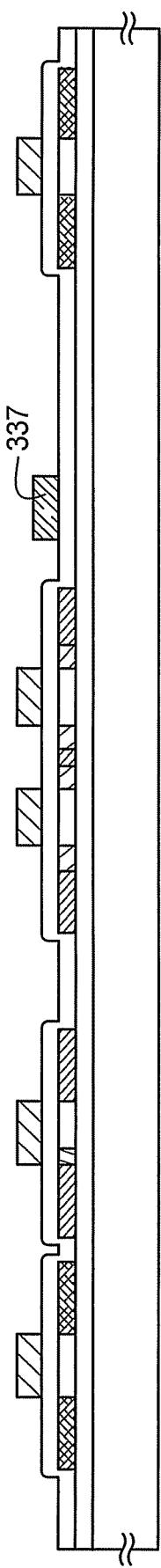


FIG. II-E. 5D

316-323:n-TYPE IMPURITY REGION (c) 324a-324d, 332:RESIST MASK 325-329:n-TYPE IMPURITY REGION (a)  
 333-336:p-TYPE IMPURITY REGION (a) 337.GATE WIRING

FIG. 6A

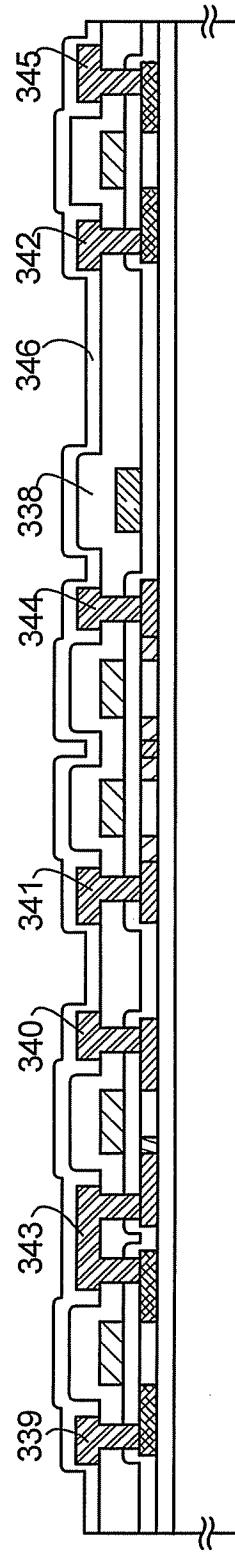


FIG. 6B

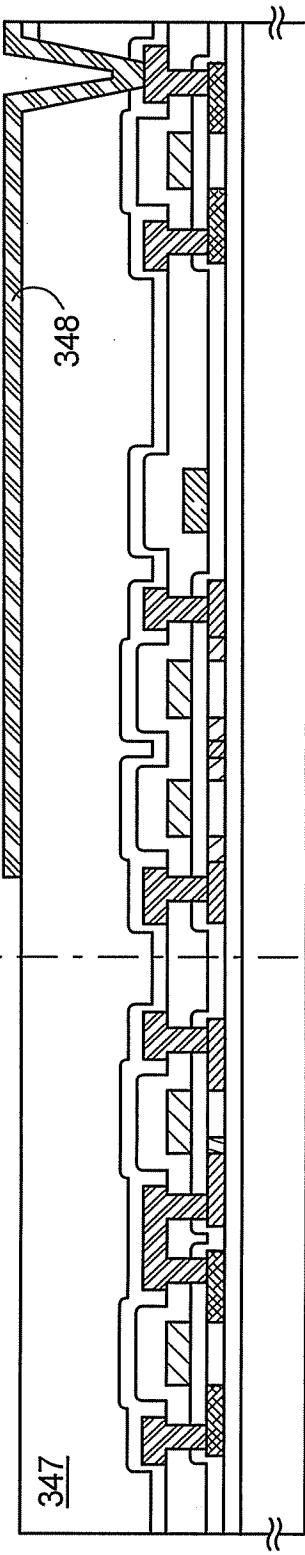
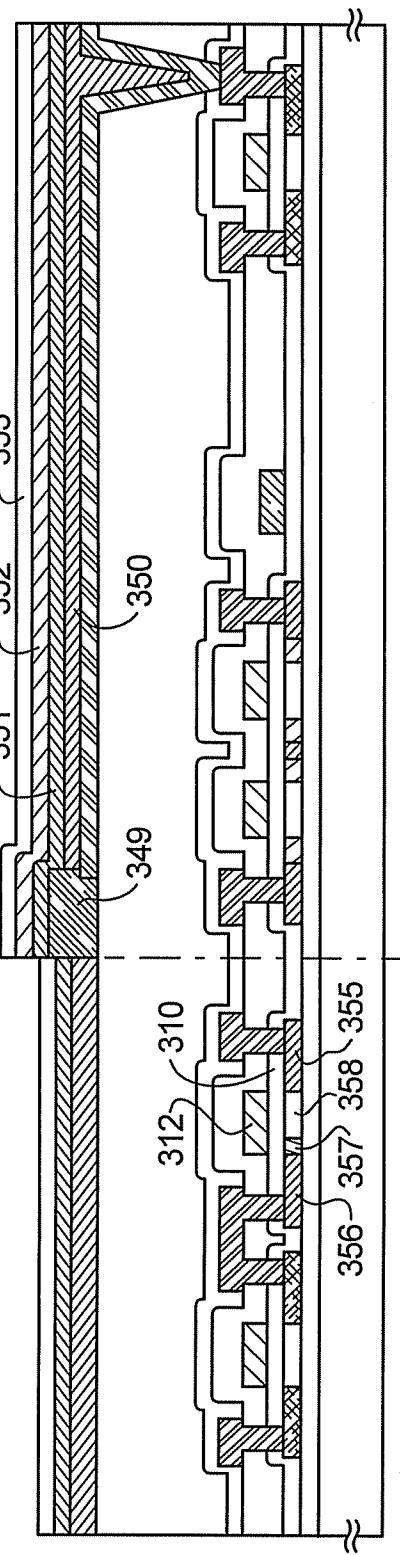


FIG. 6C



338:FIRST INTERLAYER INSULATING FILM 339-342:SOURCE WIRING 343-345:DRAIN WIRING  
 346:FIRST PASSIVATION FILM 347:SECOND INTERLAYER INSULATING FILM 348:PIXEL ELECTRODE  
 349:BANK 350:EL LAYER 351:CATHODE 352:PROTECTING ELECTRODE 353:SECOND PASSIVATION FILM

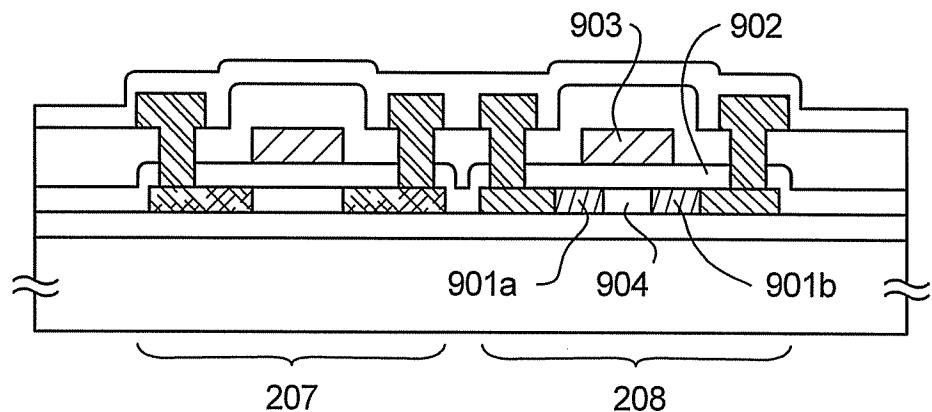
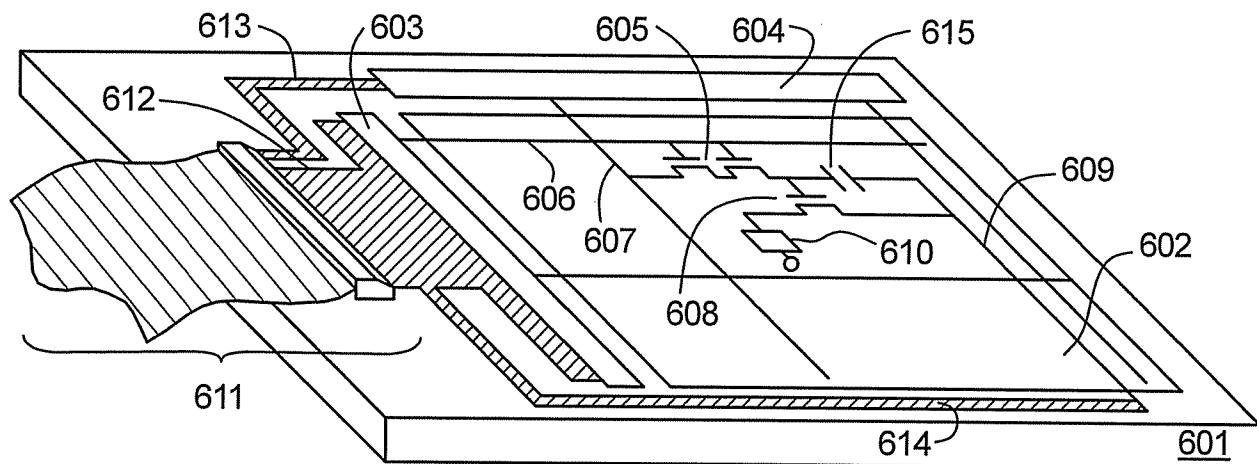


FIG. 7



601:SUBSTRATE 602:PIXEL PORTION 603:GATE SIDE DRIVER CIRCUIT  
 604:SOURCE SIDE DRIVER CIRCUIT 605:SWITCHING TFT 606:GATE WIRING  
 607:SOURCE WIRING 608:CURRENT CONTROL TFT 609:ELECTRIC POWER SUPPLY  
 610:EL ELEMENT 611:FPC 612-614:INPUT/OUTPUT TERMINAL 615:CAPACITOR

FIG. 8

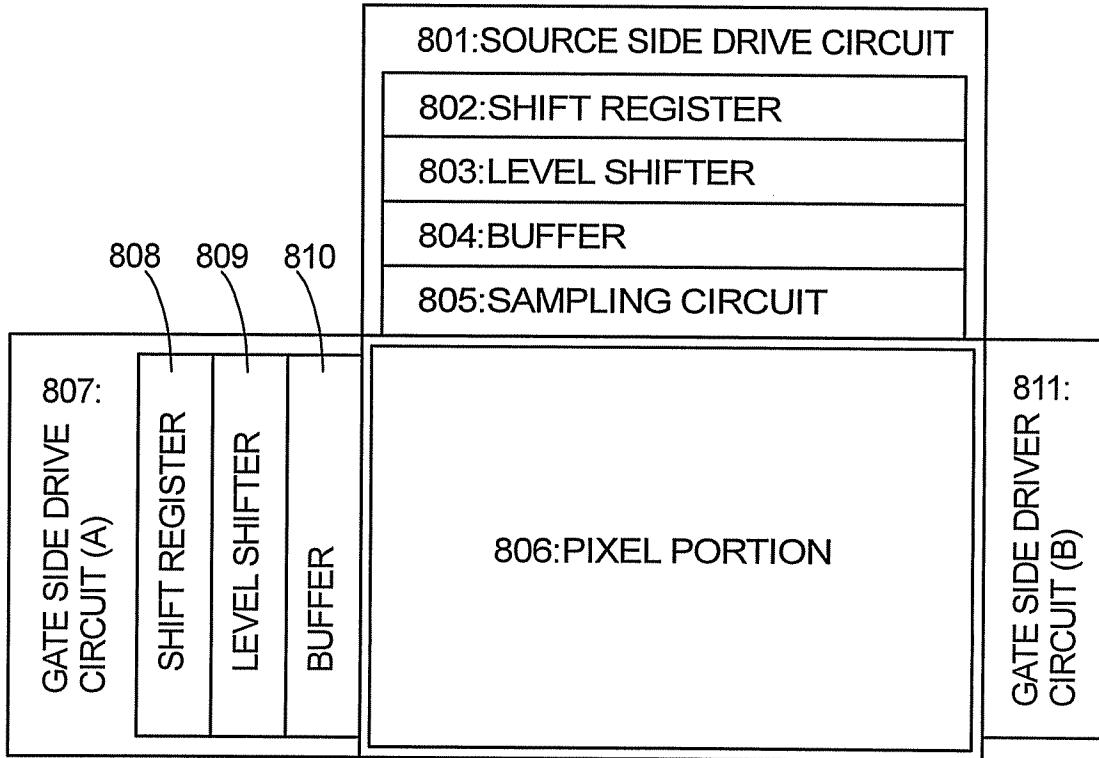


FIG. 9

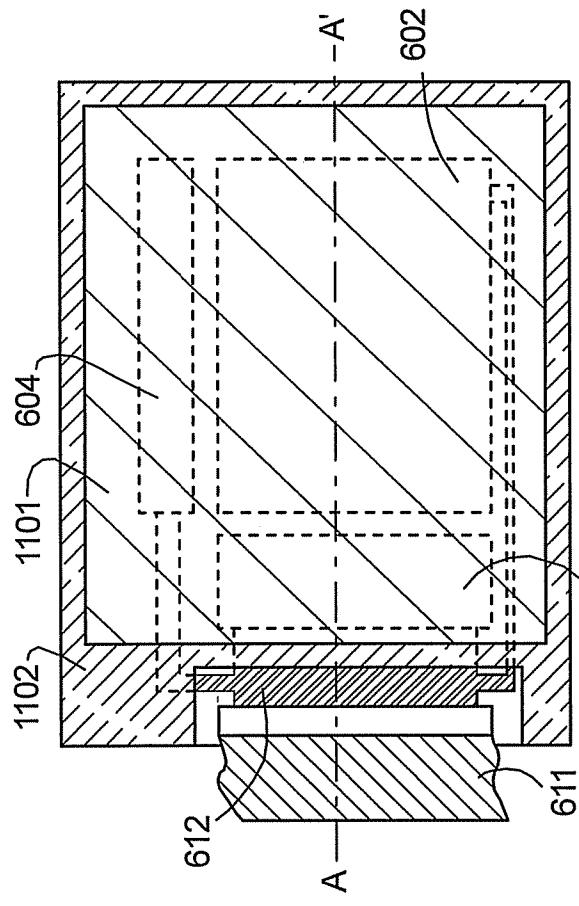


FIG. 10A

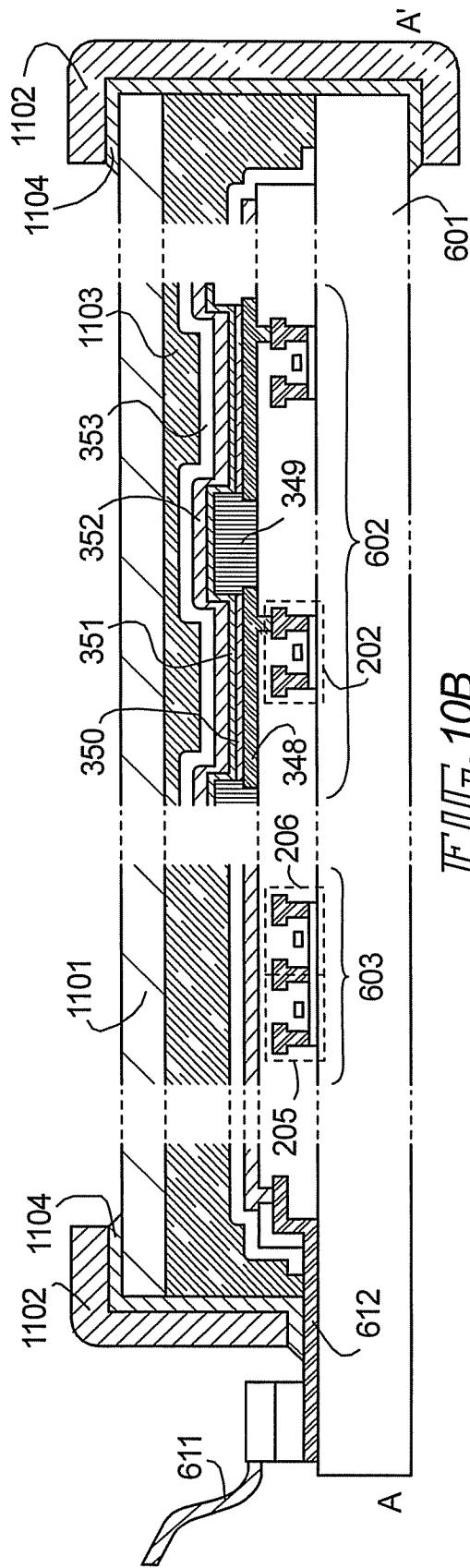


FIG. 10B

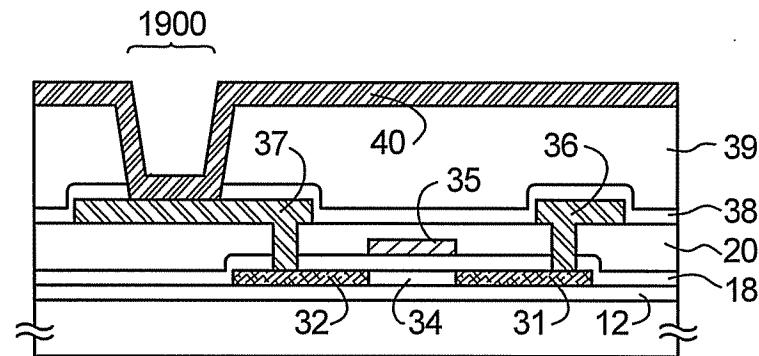


FIG. 11A

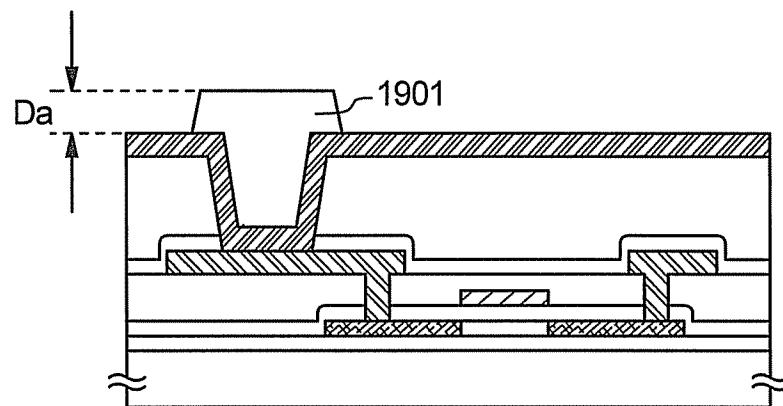


FIG. 11B

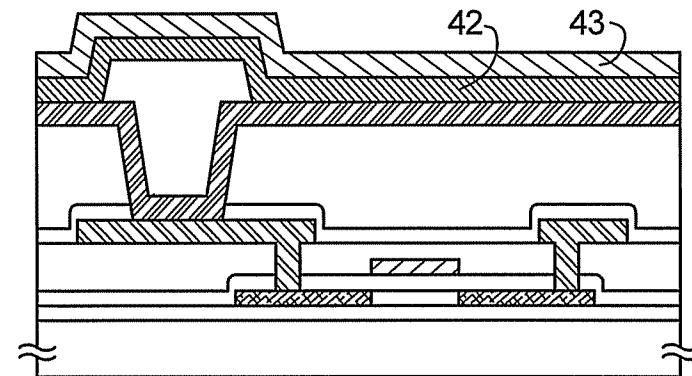


FIG. 11C



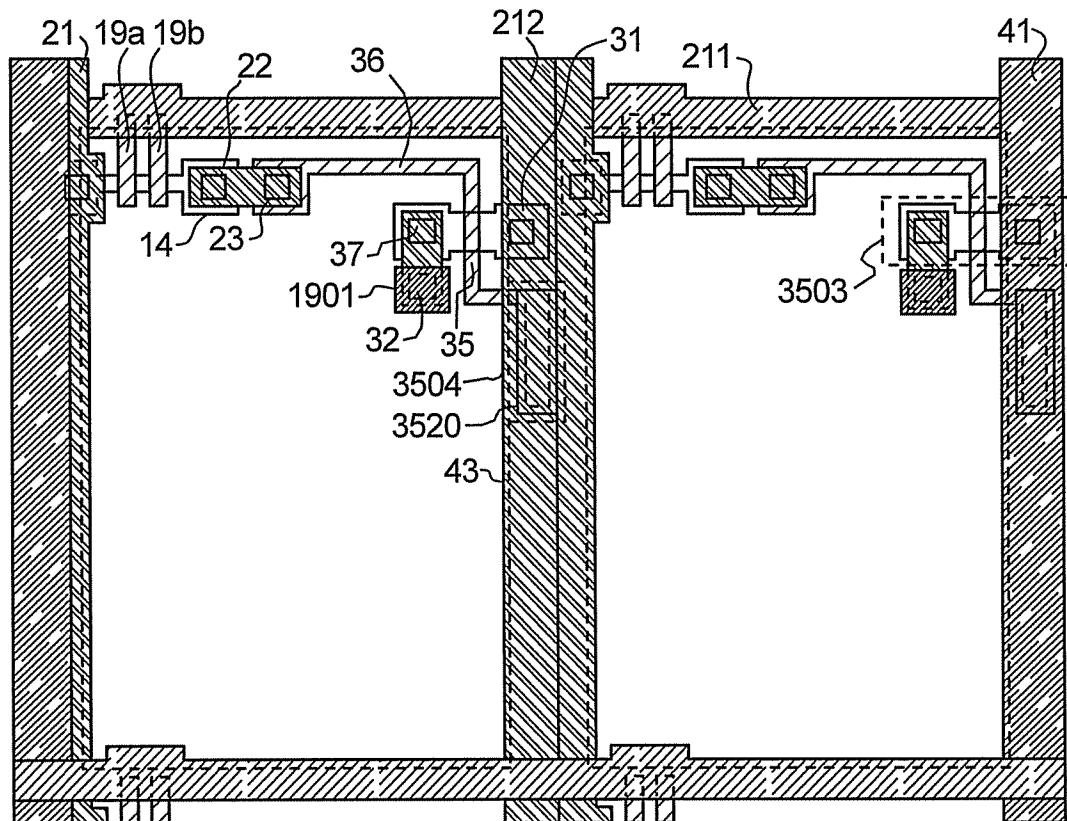
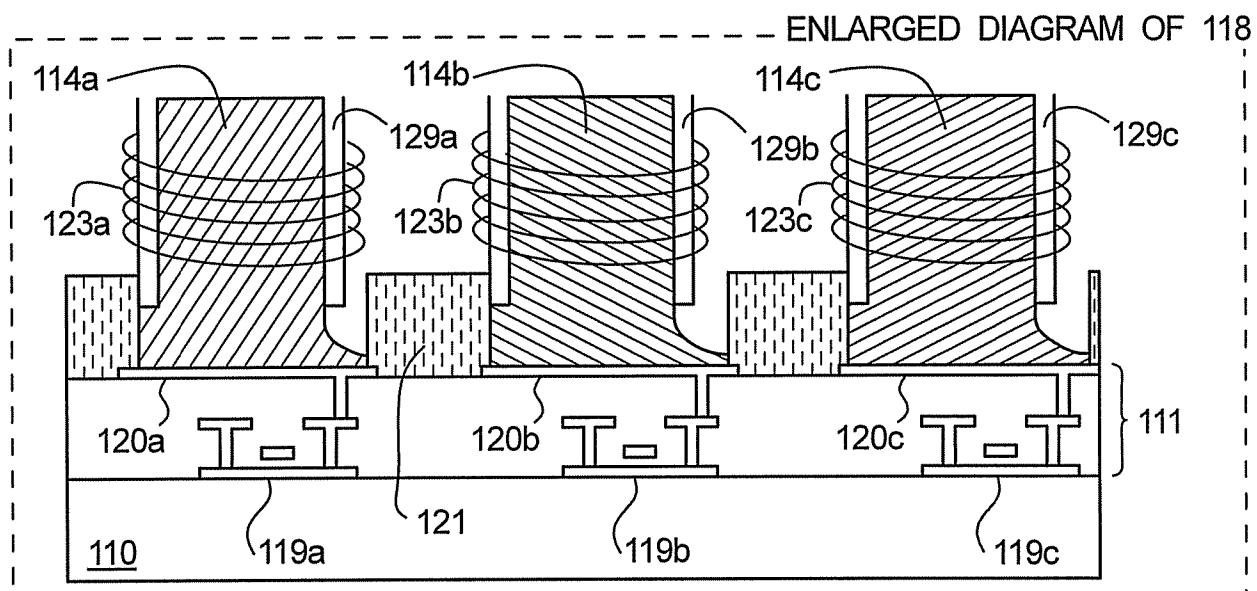
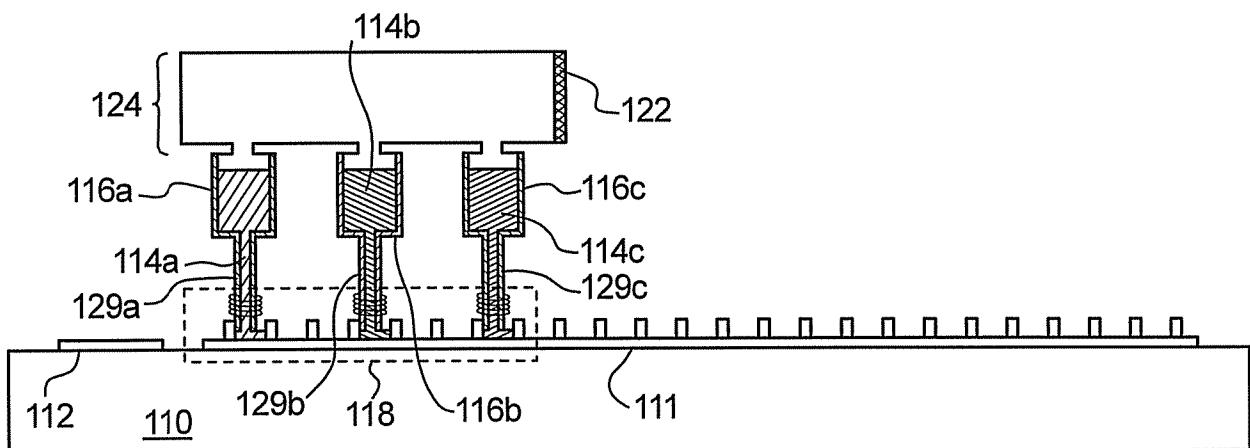
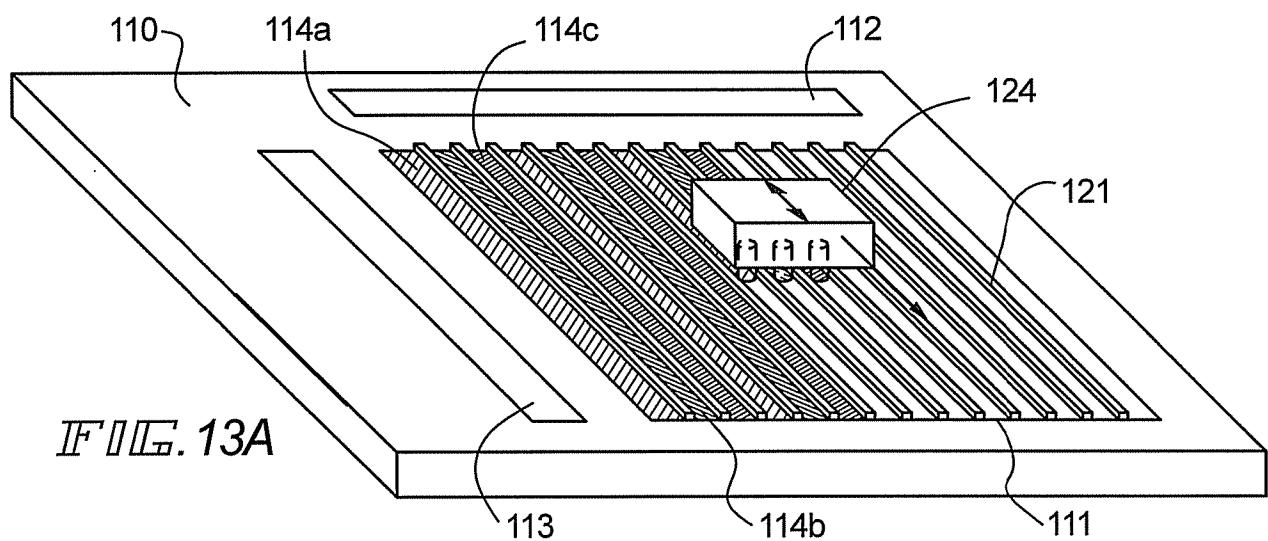


FIG. 12



**FIG. 13C**

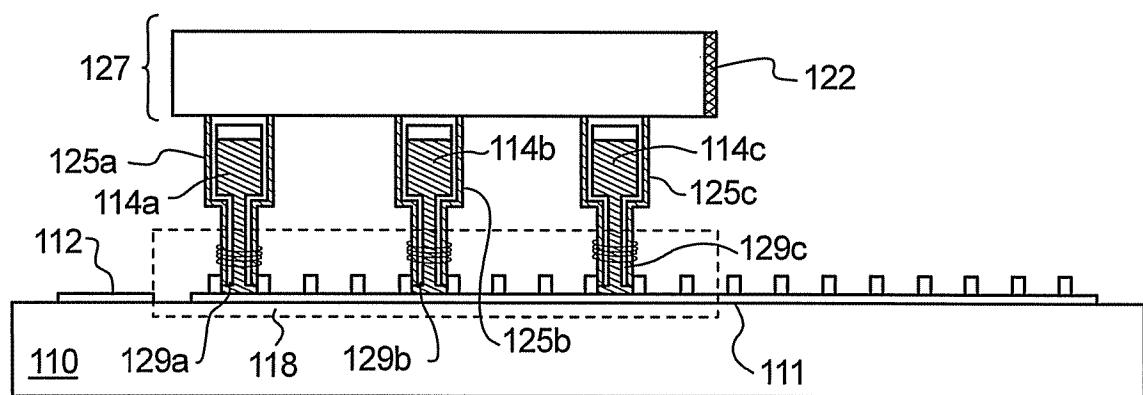


FIG. 14A

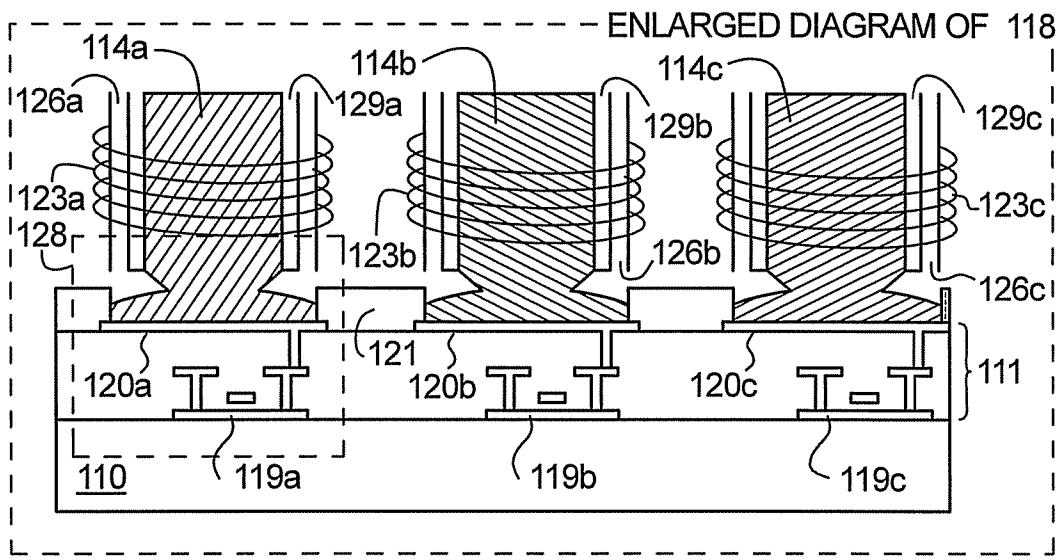


FIG. 14B

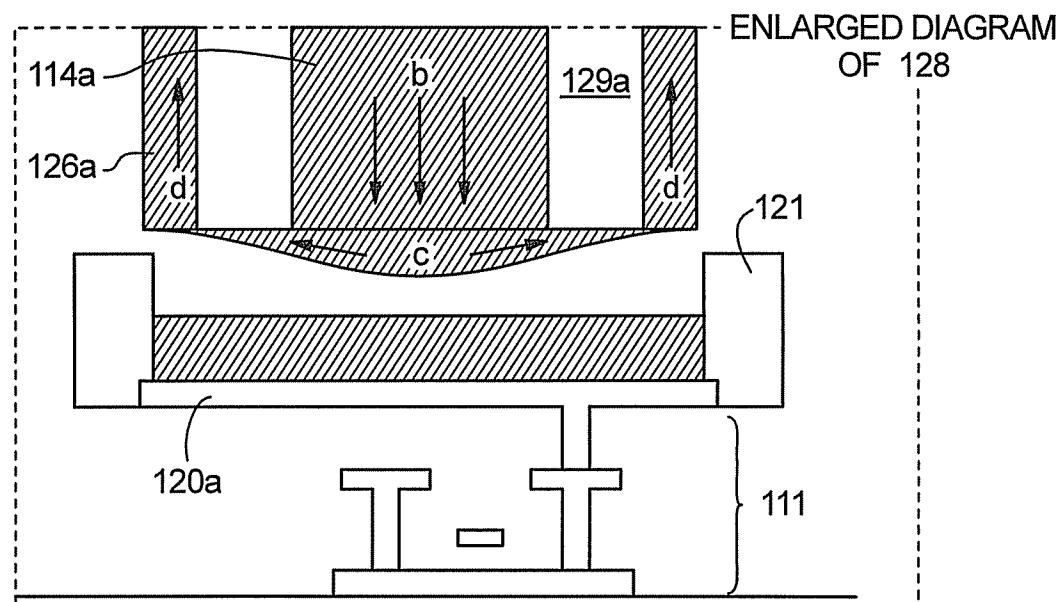


FIG. 14C

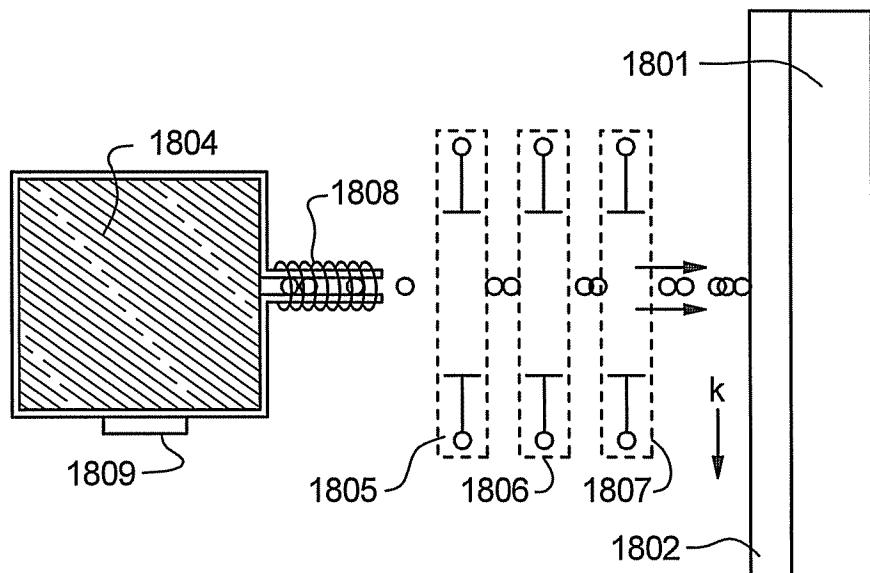


FIG. 15A

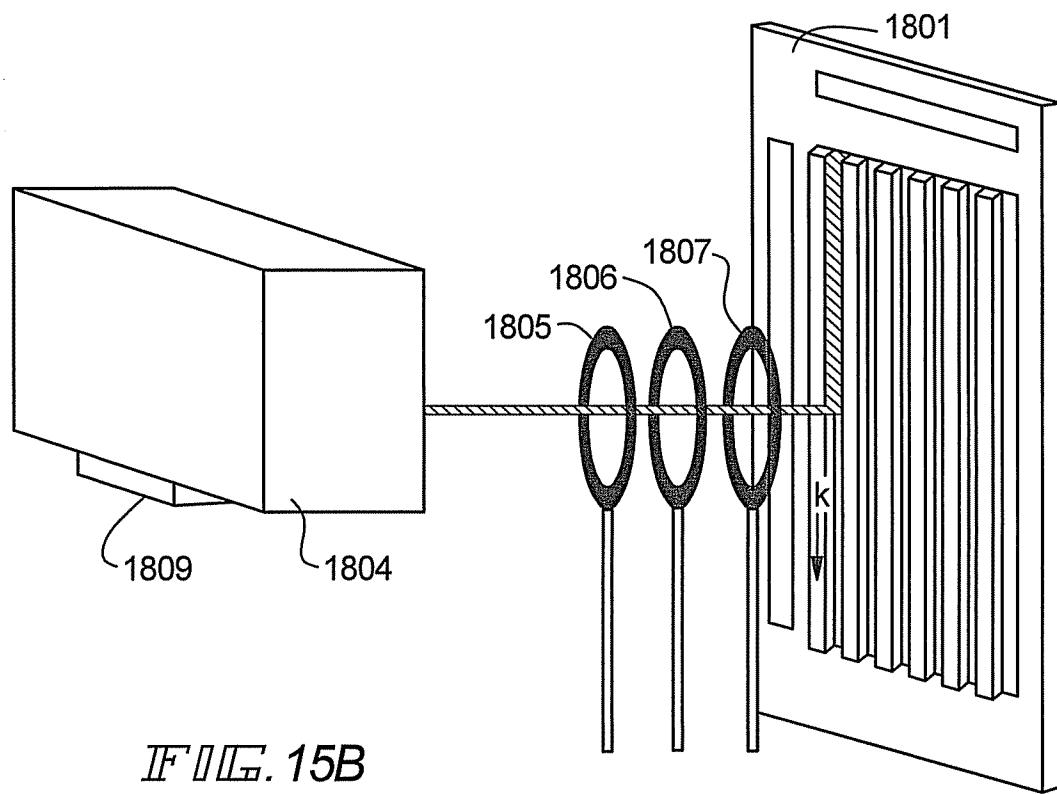
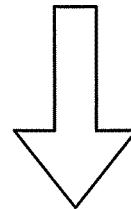


FIG. 15B



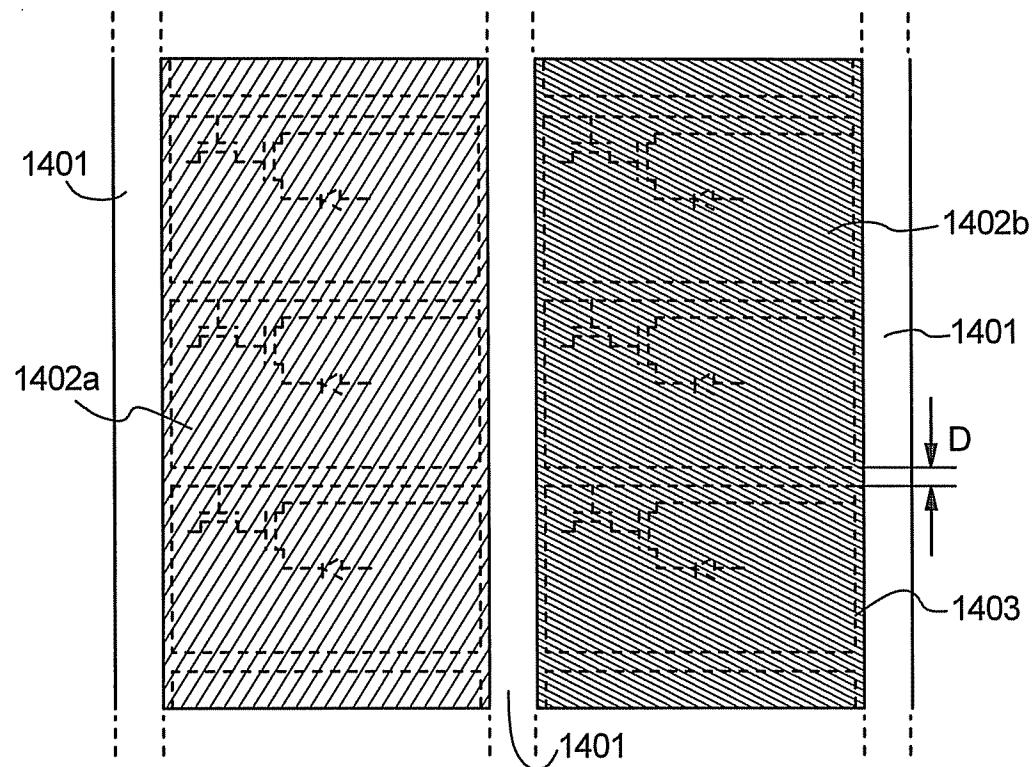


FIG. 16A

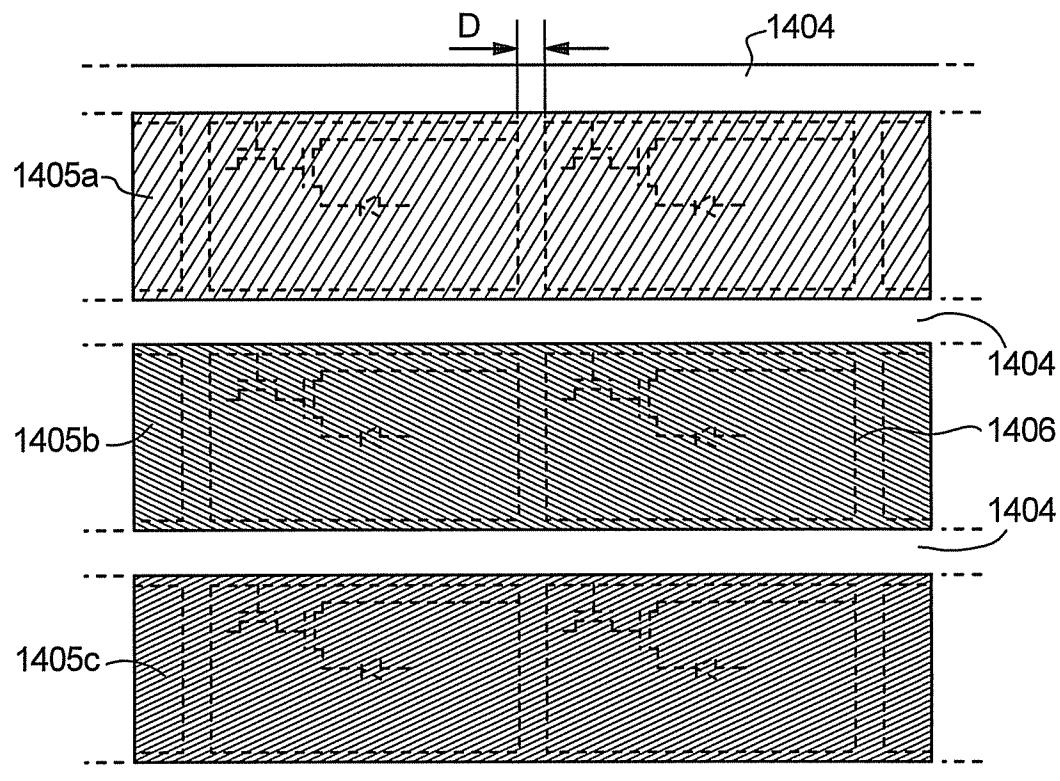


FIG. 16B

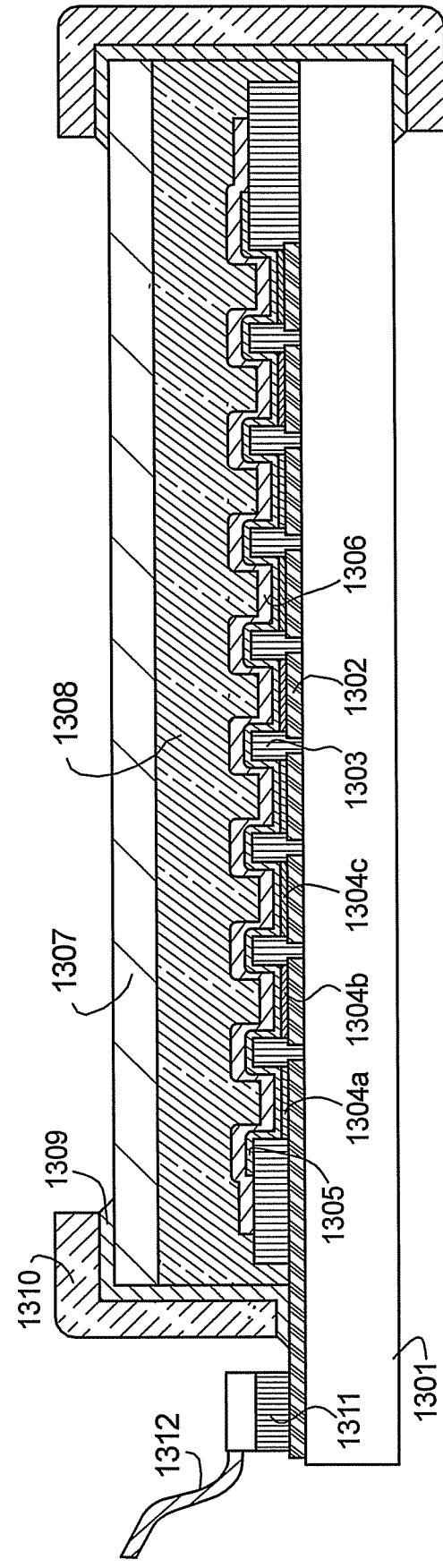
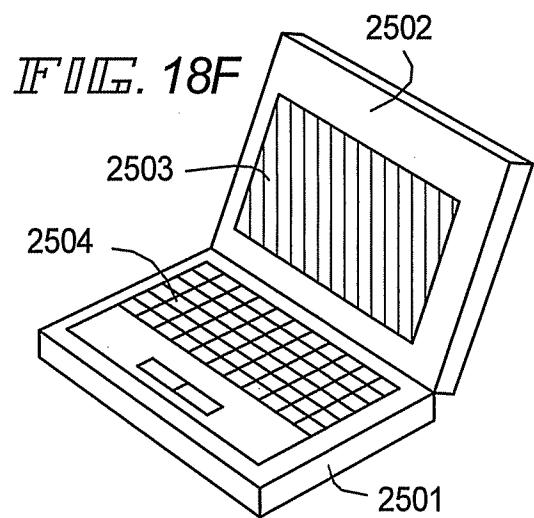
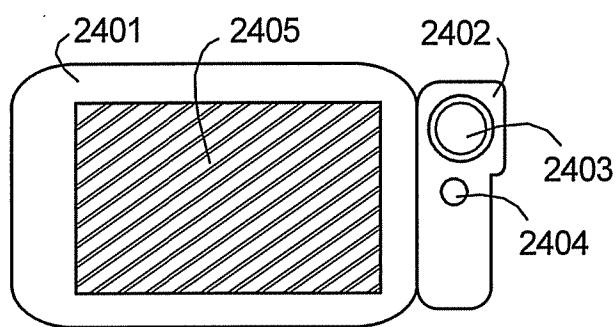
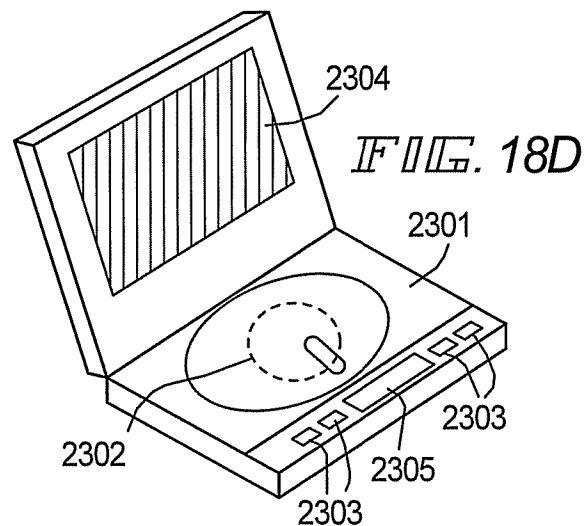
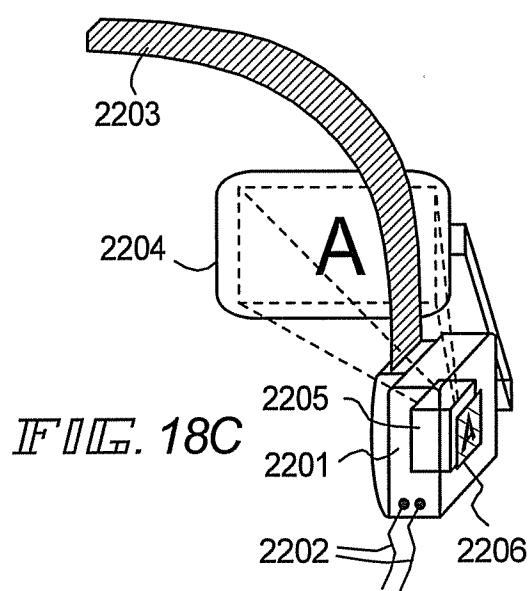
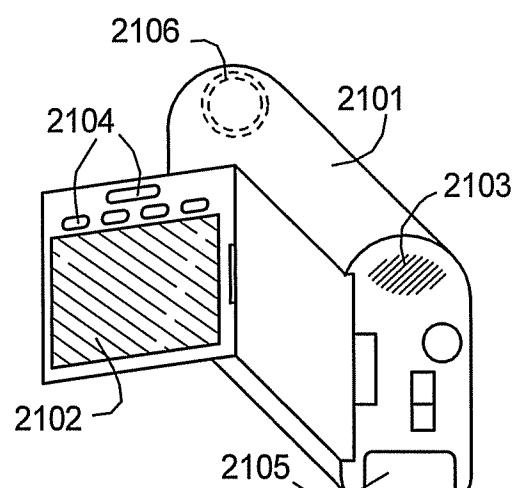
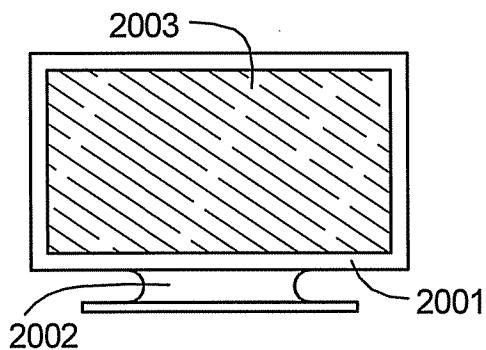


FIG. 17



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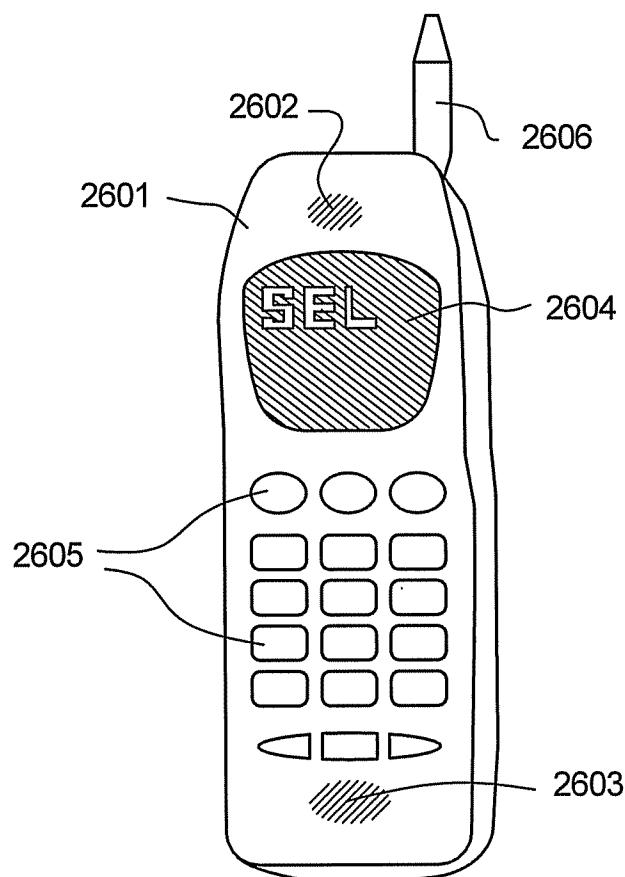


FIG. 19A

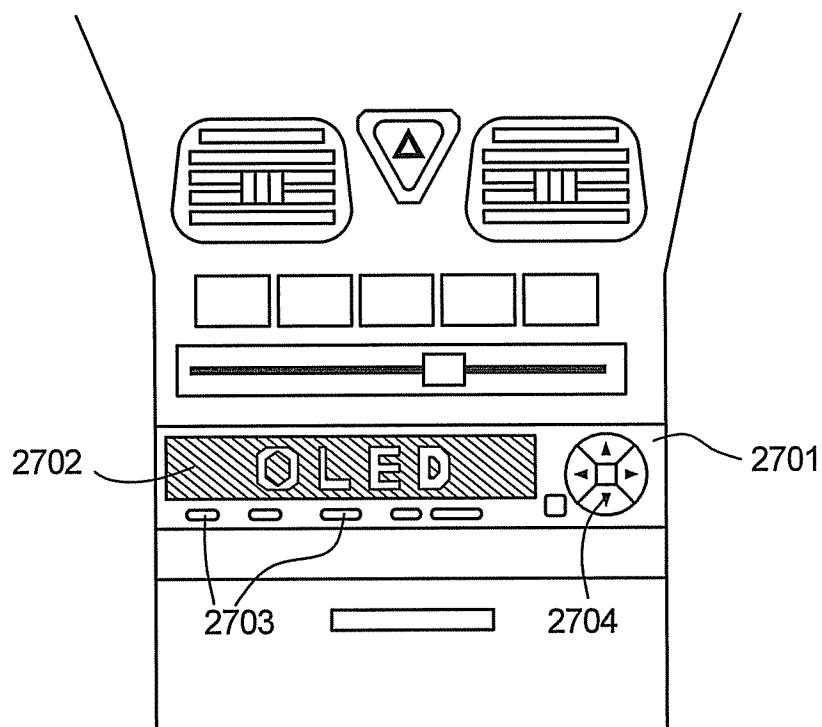


FIG. 19B

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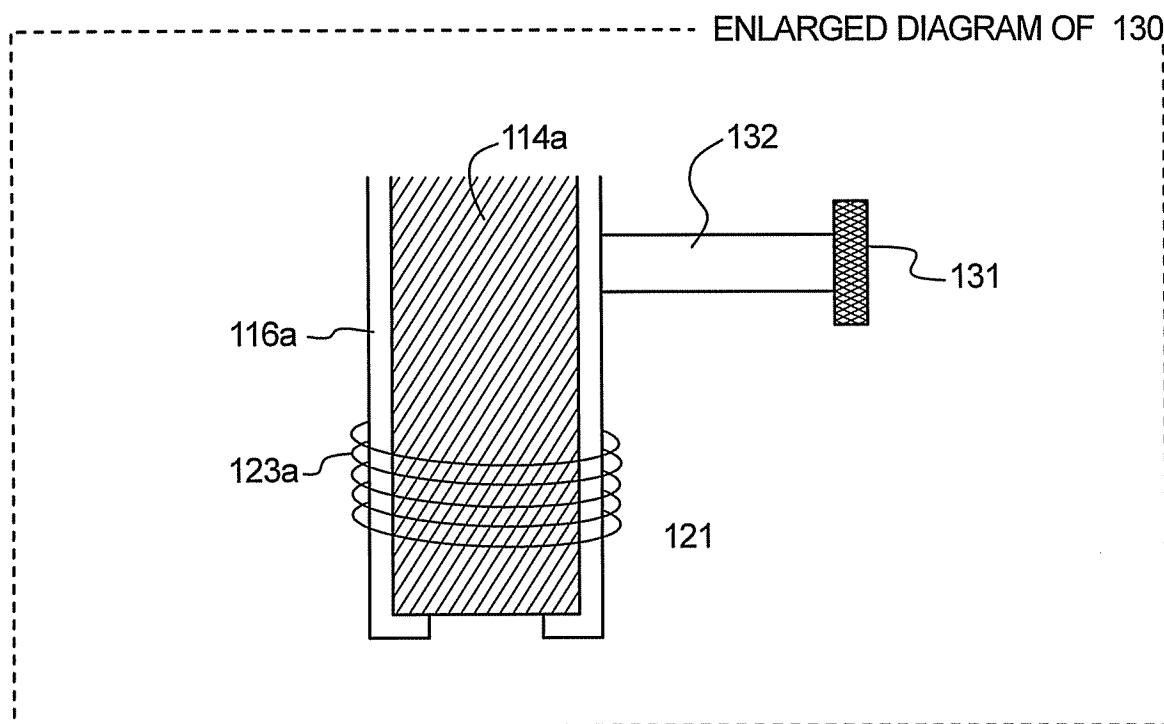
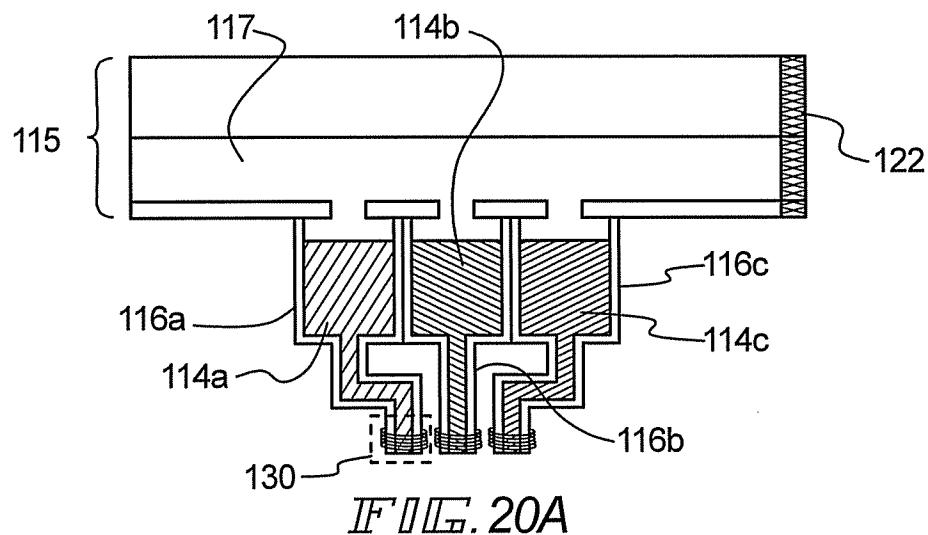


FIG. 20B